

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10575758
<b>Filing Date:</b>	05-Mar-2007
<b>Title of Invention:</b>	Printed Circuit Board Design Instruction Support Method and Device
<b>First Named Inventor/Applicant Name:</b>	Hiromichi Inaishi
<b>Filer:</b>	Paul C. Lewis/Candice Lee
<b>Attorney Docket Number:</b>	1794-0182PUS1

Filed as Large Entity

### **U.S. National Stage under 35 USC 371 Filing Fees**

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 1 month with \$0 paid	1251	1	130	130

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>130</b>